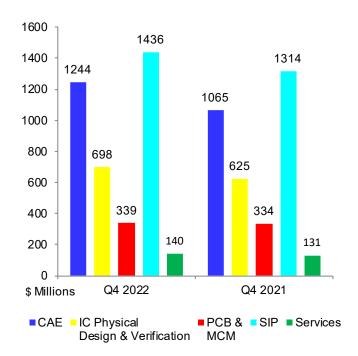
Each quarter, the ESD Alliance publishes the Electronic Design Market Data (EDMD) report (formerly the Market Statistics Service) containing detailed revenue data for the EDA, Semiconductor IP, and design services industries. The report compiles data submitted confidentially by both public and private EDA and SIP companies into tables and charts listing the data by both product category and geographic region. This newsletter highlights the results for the fourth quarter of 2022. Additional details are available in the press release. The full report is available in the SEMI Store. Details are available at https://www.semi.org/en/communities/esda/EDMD.

Overall, fourth quarter 2022 industry revenues increased 11.3% compared to Q4, 2021. Total revenue for Q4 was \$3,858.7 million. Figures 1 and 2, below, summarize the revenue growth for Q4 2022 compared to Q4 2021, detailed by category (figure 1) and geographic region (figure 2). The EDMD report contains many additional sub-categories, allowing companies to perform a more detailed analysis of revenues affecting their business. A complete list of categories is available <a href="https://example.com/here/business/



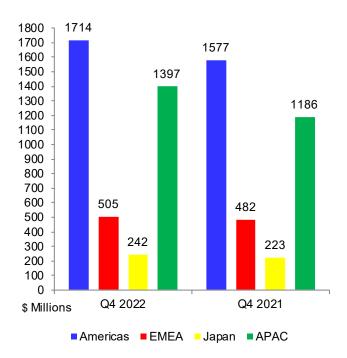


Figure 1: Q4 2022 revenue growth by category

Figure 2: Q4 2022 revenue growth by region

Tables 1 and 2 detail the percentage growth for the industry by major category (table 1) and region (table 2).

Category	Revenue (\$Millions)	% Change
CAE	1244.2	16.9%
IC Physical Design		
& Verification	698.3	11.8%
PCB & MCM	339.4	1.7%
SIP	1436.3	9.3%
Services	140.5	7.1%

Table 1: Q4 2022 revenue	growth by	category
--------------------------	-----------	----------

Region	Revenue (\$Millions)	% Change
Americas	1714.5	8.7%
EMEA	504.6	4.6%
Japan	242.1	8.6%
APAC	1397.5	17.9%
Total	3858.7	11.3%

Table 2: Q4 2022 revenue growth by region

Figure 3 (below) shows the revenue percentages by major category. In Q4 2022, Semiconductor IP remained the largest category, followed by CAE. Geographically, the Americas is the largest consumer of EDA tools, followed by Asia Pacific (APAC). The remainder divided amongst Europe, Middle East, and Africa (EMEA) and Japan, as shown in Figure 4. The quarterly EDMD report includes additional details for the Asia Pacific region.

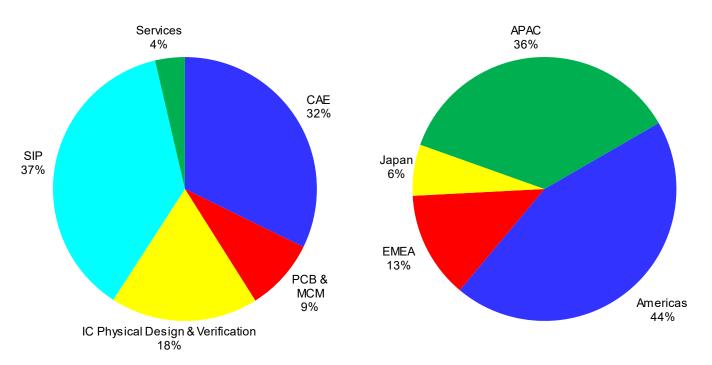


Figure 3: Q4 2022 revenue percentage by category

Figure 4: Q4 2022 revenue percentage by region

Figure 5 shows the historical revenue for the major categories (CAE, PCB & MCM, IC Physical Design and Verification, SIP and Services) from Q1 1996 through Q4 2022. Each quarter's EDMD report contains detailed data for the current year as well as the previous 3 years quarterly data in both tabular and graphical formats.

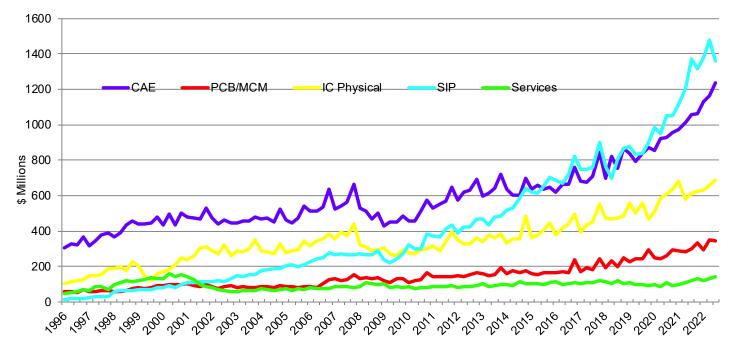


Figure 5: Revenue history, 1996 - present

Revenue data is reported confidentially to an independent accounting firm, which allows both public and private companies to report revenue data by detailed category. Individual company data is not reported, and steps are taken to further protect individual data for categories with a small number of reporting companies. Contributing data is free, and contributors will receive a discounted price for the EDMD report. The full report includes substantially more detailed information for industry revenues by category and region, providing the information companies need to analyze industry trends. The full report is available for single copy or subscription purchase in the <u>SEMI Store</u>.

<u>For more information on the EDMD report</u>, including information on contributing data, or on the benefits of joining the ESD Alliance, please visit the <u>ESD Alliance web site</u>, or <u>contact us</u>.

The ESD Alliance, a SEMI Technology Community — Where Electronics Begins	S			
The ESD Alliance EDMD newsletter is a publication of the ESD Alliance, a SEMI Technology Community. For more information, visit: esd-alliance.org				
Q4, 2022				
Electronic System Design Alliance, 673 South Milpitas Blvd, Milpitas, CA 95035. Phone (408) 943-6900				
Executive Director Bob Smith	Editor Paul Cohen			
Governing Council				
Aart de Geus, Synopsys, Inc	John Kibarian, PDF Solutions			
Anirudh Devgan, Cadence Design Systems	Prakash Narain, Real Intent			
Dean Drako, IC Manage	Joseph Sawicki, Siemens EDA			
Aki Fujimura, D2S	Simon Segars, (Chair)			
Maheen Hamid, Breker Verification Systems	Bob Smith, ESD Alliance			